逢甲大學107學年度碩士班考試入學試題

編號:24 科目代碼:308

科目 電子學	適用電機工程學系	時間	90分鐘

※請務必在答案卷作答區內作答。 共2頁第1頁

1. (30%)

- (a) Please describe the "early effect" in BJT.
- (b) Please describe "channel length modulation" in MOSFET.
- (c) Please describe "body effect".
- (d) Please describe "Miller effect".
- (e) Please describe what's the "loading effect".
- (f) Please describe the characteristics of an ideal op amplifier.

2. (15%)

- (a) Sketch the hybrid- π small signal model of BJT, including output resistance.
- (b) Sketch the topology of emitter follower circuit.
- (c) Sketch the topology of common-gate amplifier

3. (15%)

- (a) What's the circuit in Fig. 1?
- (b) For an ideal op amplifier, please derive the voltage gain (V_o/V_i) in Fig. 1.
- (c) What's the value of R_1 if the voltage gain $A_v = 5$ and $R_2 = 12 \text{ k}\Omega$?

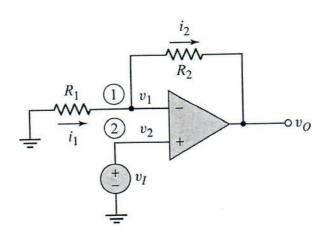


Fig. 1

4. (10%)

Consider a current mirror in Fig. 2. Assuming that all transistors are matched and neglect the effect of finite output resistances, if $V^+ = 5$ V, $V^- = 0$ V, and all the transistor of $V_{BE} = 0.7$ V, $I_{REF} = 1$ mA, and $\beta = 100$, (a) determine the value of R_1 and (b) I_{O1} .

5. (10%)

For the circuit shown in Fig. 3, both diodes are identical. Find the value of R for which V = 50 mV.

6. (20%)

Design the circuit of Fig. 4 to establish a drain current of 0.1 mA and a drain voltage of +0.3 V. The MOSFET has $V_t = 0.5$ V, $\mu_n C_{ox} = 400$ μ A/V², L = 0.4 μ m, and W = 5 μ m. Specify the required values for R_S and R_D .

