考試科目	作業系統 (Operating System) 81412	所別	資訊科學系一般生	考試時間	2 月 28 日（日）第二節

1. a. (6%) What were the three main requirements considered while designing VMware Virtual Machine Monitor (or hypervisor)?
   b. (4%) Give an example of IaaS (Infrastructure as a Service), PaaS (Platform as a Service), and SaaS (Software as a Service).

2. a. (4%) A 255-GB disk has 65,536 cylinders with 255 sectors per track and 512 bytes per sector. How many platters and heads does this disk have?
   b. (4%) Assuming an average cylinder seek time of 11 *ms*, average rotational delay of 7 *ms* and reading rate of 100 MB/sec, calculate the average time in *ms* it will take to read 100 KB from one sector.

3. (8%) Consider a computer system that has cache memory, main memory (RAM) and disk, and an operating system that uses virtual memory. It takes 2 *ns* to access a word from the cache, 10 *ns* to access a word from the RAM, and 10 *ms* to access a word from the disk. If the cache hit rate is 95% and main memory hit rate (after a cache miss) is 99%, what is the average time in *ns* to access a word?

4. (8%) Suppose that a 10-MB file is stored on a disk on the same track (track 50) in consecutive sectors. The disk arm is currently situated over track number 100. How long will it take to retrieve this file from disk? Assume that it takes about 1 *ms* to move the arm from one cylinder to the next and about 5 *ms* for the sector where the beginning of the file is stored to rotate under the head. Also, assume that reading occurs at a rate of 100 MB/sec.

5. a. (4%) What are three requirements for a solution to the critical-section problem?
   b. (6%) Consider the following solution to the critical-section problem involving two processes P_0 and P_1. Assume that the variable turn is initialized to 0. Process $P_0's$ code is presented below.

```
/* Other code*/

While (turn !=0){} /* Do nothing and wait. */
Critical Section /* … */
turn=0;

/*Other code*/
```

For process P_1, replace 0 by 1 in above code. Determine if the solution meets ***all*** the required conditions for a correct critical-section problem solution.

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6. Measurements of a certain system have shown that the average process runs for a time T before blocking on I/O. A process switch requires a time S, which is effectively wasted (overhead). For round-robin scheduling with time quantum Q, give a formula for the CPU efficiency for each of the following:
   (a). (4%) $S < Q < T$
   (b). (4%) $Q=\infty$

7. (8%) You are given the following data about a virtual memory system:
   (a). The TLB (Translation Lookaside Buffer) can hold 1024 entries and can be accessed in 1 clock cycle (1 *ns*).
   (b). A page table entry can be found in 100 clock cycles or 100 *ns*.
   (c). The average page replacement time is 6 *ms*.
   If page references are handled by the TLB 99% of the time, and only 0.01% lead to a page fault, what is the effective address-translation time in clock cycle (or *ns*)?

8. A computer has 32-bit virtual addresses and 4-KB pages. The program and data together fit in the lowest page (0-4095). The stack fits in the highest page.
   (a). (4%) How many entries are needed in the page table if traditional (one-level) paging is used?
   (b). (4%) How many page table entries are needed for two-level paging, with 10 bits in each part?

9. (8%)A computer has four page frames. The time of loading, time of last access, and the R and M bits for each page are as shown below (the times are in clock ticks):

Page	Loaded	Last ref.	R	M
0	126	280	1	0
1	230	265	0	1
2	140	270	0	0
3	110	285	1	1

   (a) Which page will NRU replace?
   (b) Which page will FIFO replace?
   (c) Which page will LRU replace?
   (d) Which page will enhanced second chance replace?

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10. (a). (4%) One way to use contiguous allocation of the disk and not suffer from holes is to compact the disk every time a file is removed. Since all files are contiguous, copying a file requires a seek and rotational delay to read the file, followed by the transfer a full speed. Writing the file back requires the same work. Assuming a seek time of 5 *ms*, a rotational delay of 4 *ms*, a transfer rate of 8 MB/sec, and average file size of 8 KB, how long (in *ms*) does it take to read a file into memory and then write it back to the disk at a new location?

    (b). (4%) Using these numbers, how long (in second) would it take to compact half of a 16-GB disk?

11. Suppose that a system uses DMA for data transfer from disk controller to main memory. Further assume that it takes t_1 nsec on average to acquire the bus and t_2 nsec to transfer one word over the bus ($t_1 \gg t_2$). After the CPU has programmed the DMA controller, how long will it take to transfer 1000 words from the disk controller to main memory, if

    (a). (4%) word-at-a-time mode is used.

    (b). (4%) burst mode is used.

    Assume that commanding the disk controller requires acquiring the bus to send one word and acknowledging a transfer also requires acquiring the bus to send one word.

12. (8%) A system has four processes and five allocatable resources. The current allocation and maximum needs are as follows:

	Allocated	*Maximum*	*Available*
Process A	1 0 2 1 1	1 1 2 1 3	0 0 ***x*** 1 1
Process B	2 0 1 1 0	2 2 2 1 0	
Process C	1 1 0 1 0	2 1 3 1 0	
Process D	1 1 1 1 0	1 1 2 2 1	

What is the smallest value of ***x*** for which this is a safe state?

備註	一、作答於試題上者，不予計分 二、試題請隨卷繳交。